


**Remarks**

This is a divisional of USSN 09/497,717. Claims 1-21, drawn to a PLL frequency synthesizer, were elected in the parent application and are canceled. Claims 22-32, drawn to a method for calibrating a local oscillator, remain pending.

Applicant believes claims 22-32 to in allowable form, and respectfully requests examination of this application at the Examiner's earliest convenience.

Respectfully submitted,  
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